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Date: November 19, 2001

Fax # 703-308-7751

Pages 3

From: SCULLY, SCOTT, MURPHY & PRESSER

Re: Toshihiko Nakamura

U.S. Patent Appln. No.: 09/854,159

APPARATUS AND METHOD FOR PRODUCING

A PERFORMANCE EVALUATION MODEL

Our Docket: 14612

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COMMENTS:

The Filing Receipt for the above-identified Patent Application has the (A) in the title missing, it should read:

Title: Apparatus and method for producing a performance evaluation model.

Please send to us a corrected Filing Receipt with the information as it is shown on the pages to follow.

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| APPLICATION NUMBER | FILING DATE | GRP ART UNIT | FIL FEE REC'D | ATTY. DOCKET NO | DRAWINGS | TOT CLAIMS | IND CLAIMS |
|--------------------|-------------|--------------|---------------|-----------------|----------|------------|------------|
| 09/854,159 | 05/11/2001 | 2857 | 1210 | 14612 | 8 | 7 | 6 |

CONFIRMATION NO. 9714

UPDATED FILING RECEIPT



OC000000007037928

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Date Mailed: 11/07/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Assignment For Published Patent Application

NEC Corporation, Tokyo, JAPAN;

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2000-138391 05/11/2000

If Required, Foreign Filing License Granted 07/11/2001

Projected Publication Date: 02/14/2002

Non-Publication Request: No

Early Publication Request: No

Title

Apparatus and method for producing performance evaluation model

Preliminary Class

→ a

APPARATUS AND METHOD FOR PRODUCING PERFORMANCE
EVALUATION MODEL

BACKGROUND OF THE INVENTION

5 Technical Field

The present invention relates to an apparatus for producing a performance evaluation model and a method for producing a performance evaluation model, and more particularly, to an apparatus and method for producing a performance evaluation model from a Unified Modeling Language (UML) model.

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Background Art

It is desirable to shorten the amount of time from service analysis to realization due to increasingly short product cycles. In addition, in the field of integrated devices, systems are being required to deliver more functions and higher performance due to proliferation of data terminals, and the need has arisen for system design that intimately incorporates both hardware and software due to the appearance of system large-scale integration (LSI).

Under such circumstances, there is no hope of shortening the amount of time if evaluations are performed after deciding on system configuration and completing mounting. Therefore, it has become necessary to estimate performance and so forth during the course of determining system configuration without mounting. Performance evaluation tools using queuing theory are being used to respond to this need. In the case of such performance evaluation tools, a performance evaluation model is produced in which jobs input to a system are assigned to a processor using queuing, and